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February 16, 2005

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Certificate
MAR 1 5 2005
of Correction

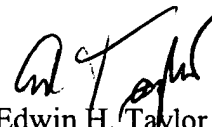
Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 6,836,816
Issued: December 28, 2004
For: FLASH MEMORY LOW-LATENCY CACHE
Inventor: Kendall
Our File No. 42390.P10070

Dear Sir:

Enclosed is the Certificate of Correction (two copies) for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


Edwin H. Taylor
Reg. No. 25,129

EHT/jsq
Enclosures

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,836,816

DATED : December 28, 2004

INVENTOR(S) : Kendall

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 2, at line 63, delete "Which" and insert --In--.

MAILING ADDRESS OF SENDER
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PATENT NO. 6,836,816

Certificate of Correction (PTO Form 1050)-Amended

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